OCKET NO. <u>HP1</u>0981470-1

IN THE U.S. PATENT AND TRADEMARK OFFICE **Patent Application Transmittal Letter**

ASSISTANT COMMISSIONER FOR PATENTS Washington, D.C. 20231

äir:

Transmitted herewith for filing under 37 CFR 1.53(b) is a(n): (X) Utility () Design

(X) original patent application,

() continuation-in-part application

INVENTOR(S): Fong Pong

TITLE:

E

ASYNCHRONOUS CACHE COHERENCE ARCHITECTURE IN A SHARED MEMORY

MULTIPROCESSOR WITH POINT-TO-POINT LINKS

Enclosed are:

(X)	The Declaration and Power of Attorney.		(x) signed	() u) unsigned or partially signe		
(X)	sheets of d	rawings (one set)		()	Associate Pow	er of Attorney	
()	Form PTO-1449	(X) In	formation Disclo	sure Sta	atement and Fo	orm PTO-1449	
()	Priority document(s)	(X)(Other) Assignment	of Patent Applica	ation	(fee \$	40.00	

CLAIMS AS FILED BY OTHER THAN A SMALL ENTITY							
(1) FOR	(2) NUMBER FILED	(3) NUMBER EXTRA	(4) RATE		(5) TOTALS		
TOTAL CLAIMS	20 — 20	0	X \$18	\$	0		
INDEPENDENT CLAIMS	3 — 3	0	X \$78	\$	O		
ANY MULTIPLE DEPENDENT CLAIMS	0		\$260	\$	0		
	40	760					
	\$	760					
	\$	40					
	\$	800					

to Deposit Account 08-2025. At any time during the pendency of this 800 application, please charge any fees required or credit any over payment to Deposit Account 08-2025 pursuant to 37 CFR 1.25. Additionally please charge any fees to Deposit Account 08-2025 under 37 CFR 1.16, 1.17,1.19, 1.20 and 1.21. A duplicate copy of this sheet is enclosed.

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Date of Deposit 11/19/1999

I hereby certify that this is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to: Assistant Commissioner for Parents, Washington,

D.C. 20231 Mon

Typed Name: Mark L. Becker

Respectfully submitted,

Fong Pong

Mark L. Becker

Attorney/Agent for Applicant(s)

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Date: 11/19/1999

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